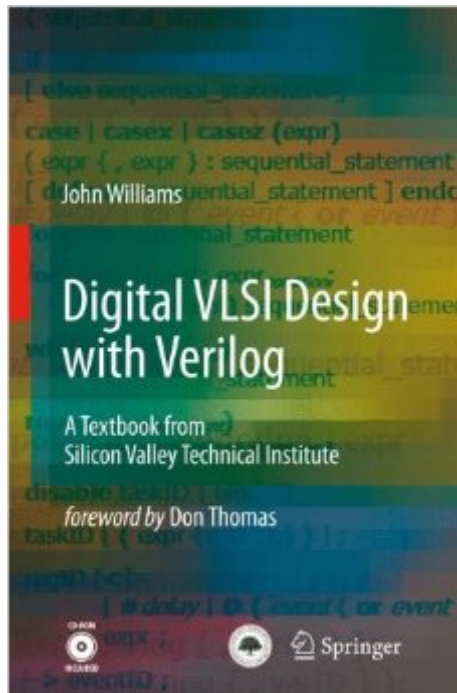


The book was found

Digital VLSI Design With Verilog: A Textbook From Silicon Valley Technical Institute



Synopsis

Verilog and its usage has come a long way since its original invention in the mid-80s by Phil Moorby. At the time the average design size was around ten thousand gates, and simulation to validate the design was its primary usage. But between then and now designs have increased dramatically in size, and automatic logic synthesis from RTL has become the standard design flow for most design. Indeed, the language has evolved and been re-standardized too.

Over the years, many books have been written about Verilog. My own, coauthored with Phil Moorby, had the goal of defining the language and its usage, providing examples along the way. It has been updated with three new editions as the language and its usage evolved. However this new book takes a very different and unique view; that of the designer. John Michael Williams has a long history of working and teaching in the field of IC and ASIC design. He brings an in-depth presentation of Verilog and how to use it with logic synthesis tools; no other Verilog book has dealt with this topic as deeply as he has. If you need to learn Verilog and get up to speed quickly to use it for synthesis, this book is for you. It is sectioned around a set of lessons including presentation and explanation of new concepts and approaches to design, along with lab sessions.

Book Information

Hardcover: 436 pages

Publisher: Springer; 2008 edition (June 26, 2008)

Language: English

ISBN-10: 1402084455

ISBN-13: 978-1402084454

Product Dimensions: 6.1 x 1 x 9.2 inches

Shipping Weight: 1.9 pounds (View shipping rates and policies)

Average Customer Review: 5.0 out of 5 stars See all reviews (1 customer review)

Best Sellers Rank: #2,168,394 in Books (See Top 100 in Books) #87 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI #675 in Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Design #6370 in Books > Textbooks > Computer Science > Programming Languages

Customer Reviews

This book gives a lot of examples and exercises. It covered a wide range of applications that readers may apply to regular design routines. Readers can become familiar with Verilog in a timely manner.

[Download to continue reading...](#)

Digital VLSI Design with Verilog: A Textbook from Silicon Valley Technical Institute Digital VLSI Design with Verilog: A Textbook from Silicon Valley Polytechnic Institute Digital Design (Verilog): An Embedded Systems Approach Using Verilog VLSI Chip Design with the Hardware Description Language VERILOG: An Introduction Based on a Large RISC Processor Design The Verilog PLI Handbook: A User's Guide and Comprehensive Reference on the Verilog Programming Language Interface VLSI Test Principles and Architectures: Design for Testability (The Morgan Kaufmann Series in Systems on Silicon) Digital Design with RTL Design, VHDL, and Verilog Digital Signal Processing in Vlsi (Analog Devices Technical Reference Books) Silicon Processing for the VLSI Era, Vol. 2: Process Integration Silicon VLSI Technology: Fundamentals, Practice and Modeling (Taschenbuch) Silicon Processing for the VLSI Era, Vol. 4: Deep-Submicron Process Technology Silicon VLSI Technology: Fundamentals, Practice, and Modeling Silicon VLSI Technology VLSI Fabrication Principles: Silicon and Gallium Arsenide, 2nd Edition Silicon-on-Insulator Technology: Materials to VLSI Digital Integrated Circuit Design Using Verilog and Systemverilog Digital Systems Design: A Practical Approach: The Verilog Edition Digital Design: With an Introduction to the Verilog HDL 5th Ed. By Morris Mano (International Economy Edition) Circuits, Interconnections, and Packaging for Vlsi (Addison-Wesley VLSI systems series) California Apricots: Lost Orchards of Silicon Valley (American Palate)

[Dmca](#)